

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Hans W. Klein
Assignee: Lattice Semiconductor Corporation
Title: CLOCK GENERATOR
Serial No.: 10/629,223 Filing Date: July 29, 2003
Examiner: Cox, Cassandra F. Group Art 2816
Unit:
Docket No.: M-15169 US

Fee Due
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Commissioner for Patents
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TERMINAL DISCLAIMER TO OBVIATE A PROVISIONAL
DOUBLE PATENTING REJECTION OVER A PENDING
SECOND APPLICATION

Sir:

10/14/2004 SSURLES 000 0002 50223-10000223
01 FC:1814 110.0
Petitioner, Lattice Semiconductor Corporation, a Delaware corporation having a place of business at 5555 N.E. Moore Court, Hillsboro, Oregon 97124-6421 is the owner of the entire interest in the instant application. Petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173, as presently shortened by any terminal disclaimer filed prior to the grant of any patent granted on pending second Application Number 10/629,221, filed on July 29, 2003. Petitioner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and any patent granted on the second

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